



GB 2823
JLW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of

Applicants : Sanh Dang Tang
Serial No. : 10/078,831
Filed : February 18, 2002
Title : SEMICONDUCTOR DEVICES HAVING GRADUAL SLOPE
Docket : MIO 0018 V2 (96-1138.03)
Examiner : M. Estrada
Art Unit : 2823

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on August 11, 2004.

Susan M. Luna

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COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

This paper is being filed in response to the Notice of Allowance mailed July 23, 2004. Applicants respectfully submit that at least some of the Examiner's statements in the "Reasons for Allowance" do not accurately reflect the scope of the claims. Specifically, applicants disagree with the Examiner's statement regarding the Wu reference at page 2, which is not reflected in applicant's claims. Applicants submit that the prosecution as a whole makes clear the reasons for allowance.

As such, applicants submit that the "Reasons for Allowance" should not be used to interpret the scope of the claims of the present application.

Respectfully submitted,

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